

Walter Arevalo

EDUCATION

UC Santa Cruz, Santa Cruz - B.S. Computer Engineering - Computer Systems Concentration
September 2018 - May 2023

WORK EXPERIENCE

Area Supervisor - Ross Stores Inc - Santa Clarita, CA

August 2020 - September 2021

- Lead a team of 30+ associates
- Work with other stores and their management teams
- Manage cash deposits and withdrawals
- Provide excellent customer service and be able to de escalate any situation

Technician, ResNet- Santa Cruz, CA

August 2021 - Present

- Help clients with technical questions including Network Configuration, Network Registration, and Diagnosing and Fixing Software Issues
- Provide house calls or drop off services to help clients with technical issues
- Work in a team environment to help clients more efficiently
- Diagnoses endpoint issues with enterprise network serving over 9,000 residents
- Educates clients on university IT services and policies
- Provides hands-on and remote support with desktop and mobile devices

COMPUTER SKILLS/ PROJECTS

- 2 years experience working with Python, Verilog, C, C++
- 1-year experience working with Java
- Experience working with Vivado software, Basys 3 FPGAs
- Ability to work on CPU design and register level processors
- Create a working pong game using Verilog on an FPGA. Use various gates and knowledge of basic implementations of systems using Verilog.
- Create a working microprocessor, designing, implementing, and verifying systems based on the RISC-V processor design. Observing the differences between RISC-V, ARM, and x86 processors and their register level workings.

COURSE EXPERIENCE

CSE 125 - Logic Design with Verilog

- Use behavioral and structural verilog to create various designs
- Use Verilog verification software to understand the verification of FPGA
- Optimize designs in regard to LUTs and FFs

CSE 121 - Embedded System Design

- Create a Embedded system using PSOC and Arduino
- Create Drivers for PSOCs and learn to use I2C and various protocols as well as coding in x86.