



Jim Warner <warner@ucsc.edu>

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## buffer size on 3750G

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**Anna Tsukerman (atsukerm)** <atsukerm@cisco.com>  
To: jim warner <warner@ucsc.edu>

Thu, Jan 21, 2010 at 2:19 PM

Hi Jim,

Here is the information on buffer for the 3750G-48TS:

ingress buffer is 192KB per ASIC (multiply this by 6 since we have 6 ASICs on the 48port)

egress buffer is 384KB per ASIC (multiply this by 6 since we have 6 ASICs on the 48port)

Thank you,

Anna

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